California Institute of Technology
Department of Computer Science
Computer Architecture

CS184b, Winter 2000   Day 4: CISC vs. RISC   Tuesday, January 16

Items for discussion and highlight:

- Foreign things in papers?

- Changes which underly?

- "Fit" problem

- Measurement good

- ISA Growth

- Design time?

- Big, telling problem for VAX?

- compiler specialization vs. special instructions

- old problems (unrational implementations, fast memory, interrupts)

- other pioneering ideas

- assume everything compiler generated?

- Everyone "believes" RISC today, but do they understand it?